

INFORMATION DISCLOSURE STATEMENT

Applicant	: Gajadharsing et al.
App. No	: 10/551,324
Filed	: October 18, 2005
For	: SEMICONDUCTOR TRANSISTOR (DMOS) DEVICE FOR USE AS A POWER AMPLIFIER (as amended)
Examiner	: Sarah Kate Salerno
Art Unit	: 2814

Mail Stop Amendment

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed for filing in the above-identified application is a PTO/SB/08 Equivalent listing four (4) references to be considered by the Examiner. Also enclosed are three (3) foreign patent references and/or non-patent literature as listed on the Information Disclosure Statement.

Applicant notes that Reference Nos. 2 and 3 are foreign patent references that were cited in the PCT International Search Report of this national phase application. The International Search Report is being submitted as Reference 4 and is believed to satisfy the requirements 37 C.F.R. § 1.56(c) of a concise explanation of relevance for Reference Nos. 2 and 3, which are in not in the English language (see M.P.E.P. § 609.04(a)(III)).

This Information Disclosure Statement is being filed before the mailing date of a final action and before the mailing of a Notice of Allowance. This Statement is accompanied by the fees set forth in 37 C.F.R. § 1.17(p).

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Docket No. SOROK16.001APC
Customer No. 20,995

The Commissioner is hereby authorized to charge any additional fees which may be required or to credit any overpayment to Account No. 11-1410.

Respectfully submitted,
KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: 5/15/2008

By: Christian A. Fox
Christian A. Fox
Registration No. 58,507
Attorney of Record
Customer No. 20,995
(949) 760-0404

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